



Receipt
#2

PATENT
5201-24300/01-120

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Hsiung et al.

Serial No. 09/885,687

Filed: June 19, 2001

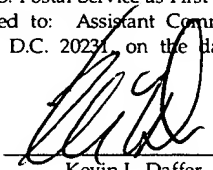
For: SEMICONDUCTOR DEVICE
PACKAGE SUBSTRATE
PROBE FIXTURE

Group Art Unit: 2862

Atty. Dkt. No. 5201-24300

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date indicated below:

August 31, 2001
Date


Kevin L. Daffer

REQUEST FOR CORRECTED FILING RECEIPT

Assistant Commissioner for Patents
Office of Initial Patent Examination
Customer Service Center
Washington, D.C. 20231

Sir:

The Filing Receipt dated August 15, 2001 has a spelling error in the title. The first word spelled "Semicondutor" should be spelled "Semiconductor." I have provided a copy of the Filing Receipt with the change noted thereon. Applicants request this change be made and a corrected Filing Receipt be issued.

Respectfully submitted,



Kevin L. Daffer
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Date: August 31, 2001



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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/885,687	06/19/2001	2862	710	5201- 2430001- 120	6	20	3

CONFIRMATION NO. 6374

FILING RECEIPT



OC00000006430149

 Gary Goates
 Patent Legal Department
 MS D-106
 1551 McCarthy Blvd.
 Milpitas, CA 95035

Date Mailed: 08/15/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

 Steve K. Hsiung, Fremont, CA;
 Kevan V. Tan, San Jose, CA;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 08/15/2001

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

semiconductor

Semiconductor device package substrate probe fixture

Preliminary Class


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